

Notic of Referenc s Cit d	Application/Control No. 09/696,955	Applicant(s)/Patent Under Reexamination TAMURA ET AL.	
	Examiner Matthew A. Anderson	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
	A	US-6284093 B1	09-2001	Ke et al.	156	345
	B	US-6299982 B1	10-2001	Tamatsuka et al.	428	446
	C	US-6190495 B1	02-2001	Kubota et al.	156	345
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: rocess Technology, Lattice Press, Sunset Beach, CA, USA, pp. 8, 23-27, 32-33, 59, 1986.					
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.